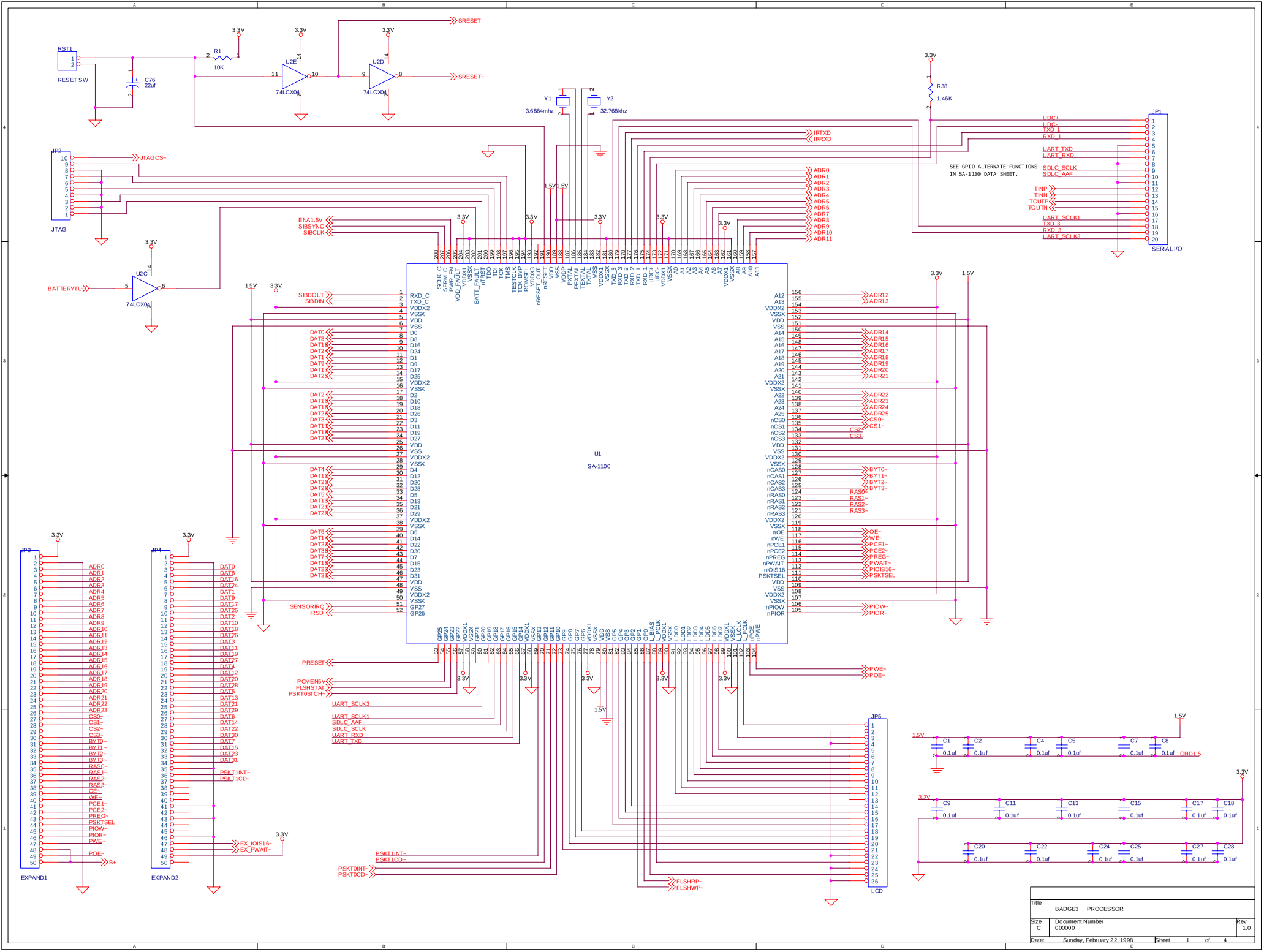
RST1
RESET SW
C76
22uf
R1
10K
3.3V
U2E
74LCX04
U2D
74LCX04
SRESET
SRESET~
Y1
3.6864mhz
Y2
32.768khz
R38
1.46K
JP1
SERIAL I/O
UDC+
UDC-
TXD_1
RXD_1
UART_TXD
UART_RXD
SDLC_SCLK
SDLC_AAF
TINP
TINN
TOUTP
TOUTN
UART_SCLK1
TXD_3
RXD_3
UART_SCLK3
SEE GPIO ALTERNATE FUNCTIONS
IN SA-1100 DATA SHEET.
IRTXD
IRRXD
JP2
JTAG
JTAGCS~
U2C
74LCX04
BATTERYTU
ENA1.5V
SIBSYNC
SIBCLK
SIBDOUT
SIBDIN
SENSORIRQ
IRSD
PRESET
PCMEN5V
FLSHSTAT
PSKT0STCH~
UART_SCLK3
UART_SCLK1
SDLC_AAF
SDLC_SCLK
UART_RXD
UART_TXD
PSKT1INT~
PSKT1CD~
PSKT0INT~
PSKT0CD~
FLSHRP~
FLSHWP~
PWE~
POE~
U1
SA-1100
JP3
EXPAND1
JP4
EXPAND2
EX_IOIS16~
EX_PWAIT~
JP5
LCD
1.5V
GND1.5
C1
C2
C4
C5
C7
C8
C9
C11
C13
C15
C17
C18
C20
C22
C24
C25
C27
C28
0.1uf
Title
BADGE3 PROCESSOR
Size
C
Document Number
000000
Rev
1.0
Date: Sunday, February 22, 1998
Sheet 1 of 4